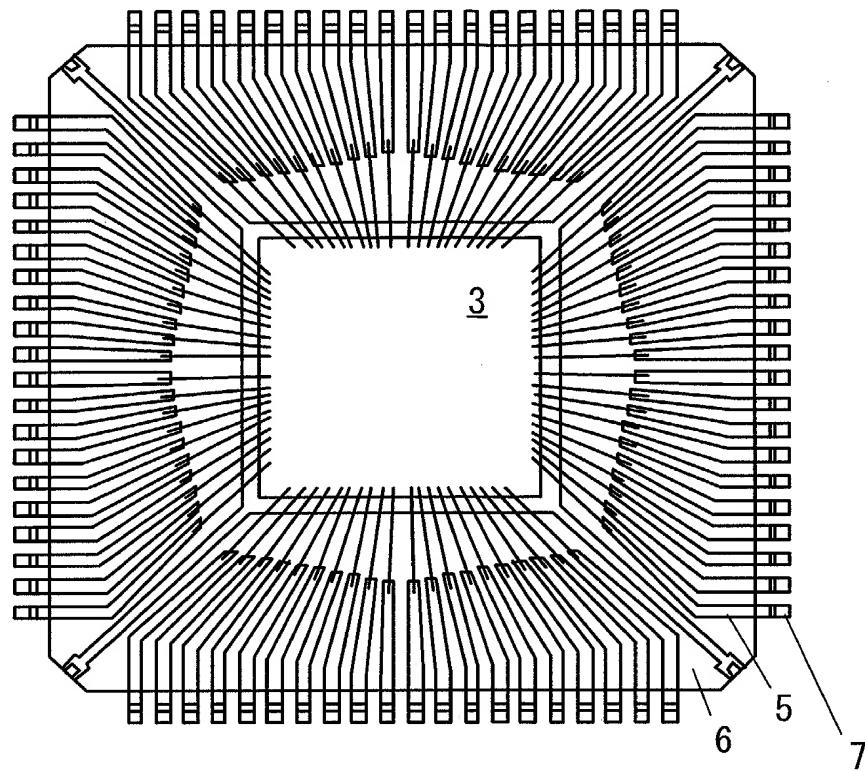
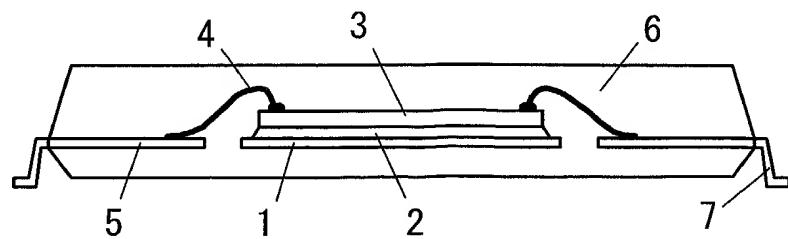


*FIG. 1A
(Prior Art)*



*FIG. 1B
(Prior Art)*



*FIG. 2
(Prior Art)*

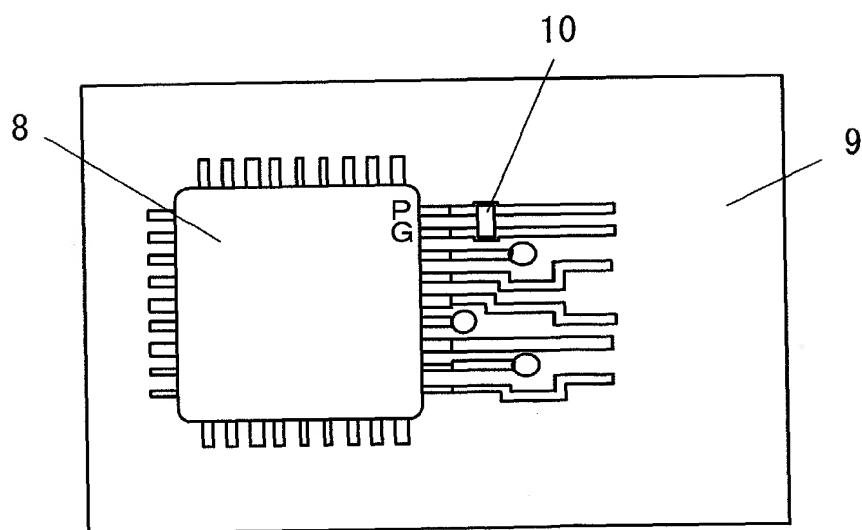


FIG. 3

PRINTED CIRCUIT BOARD LAY-OUT

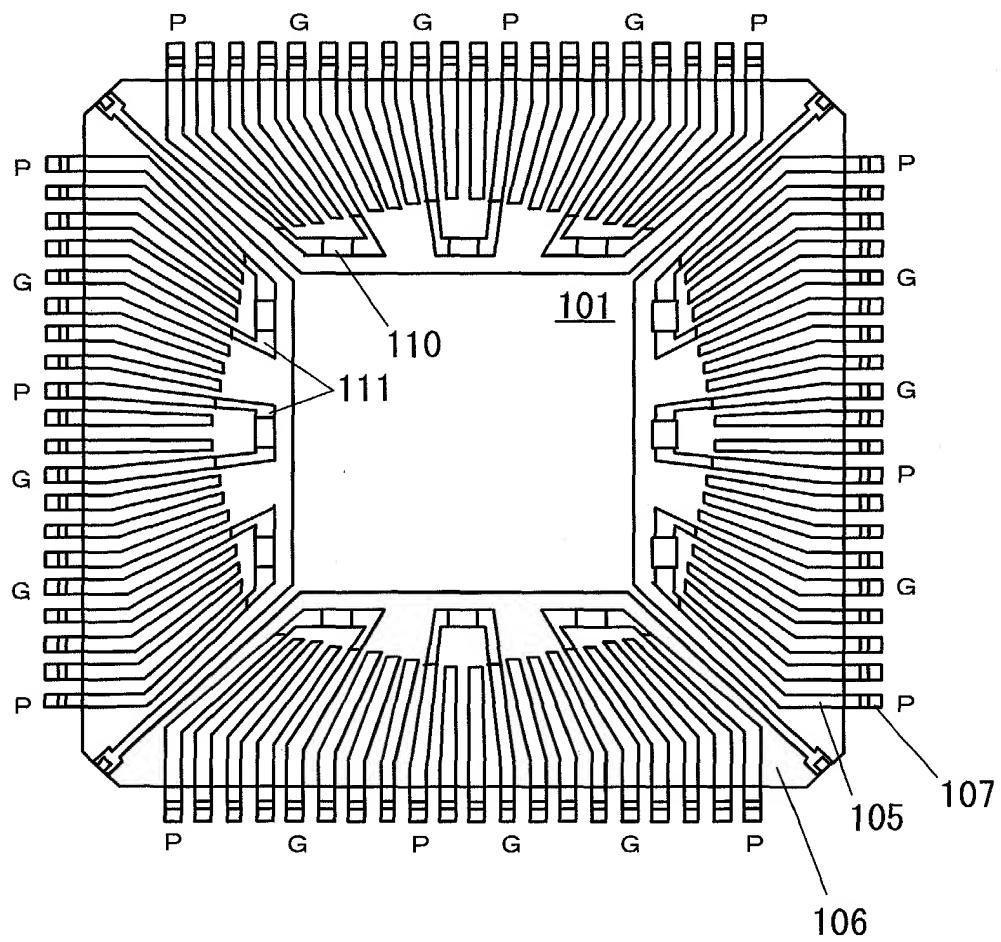


FIG. 4

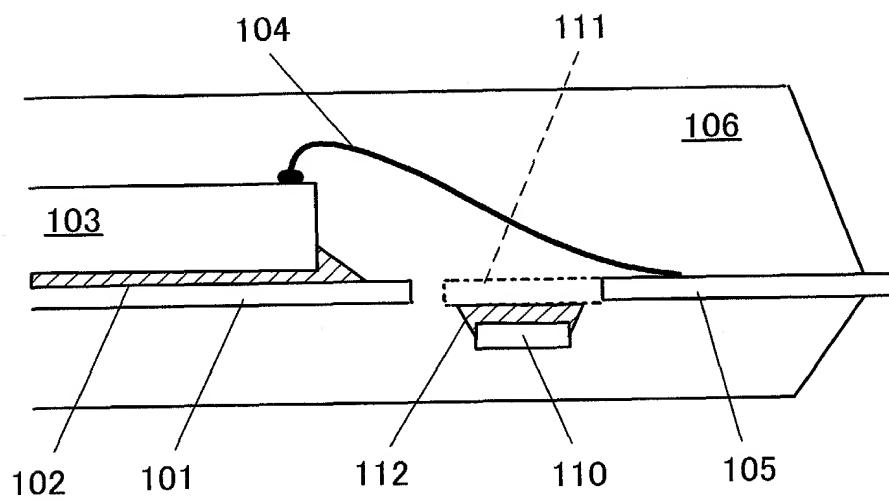


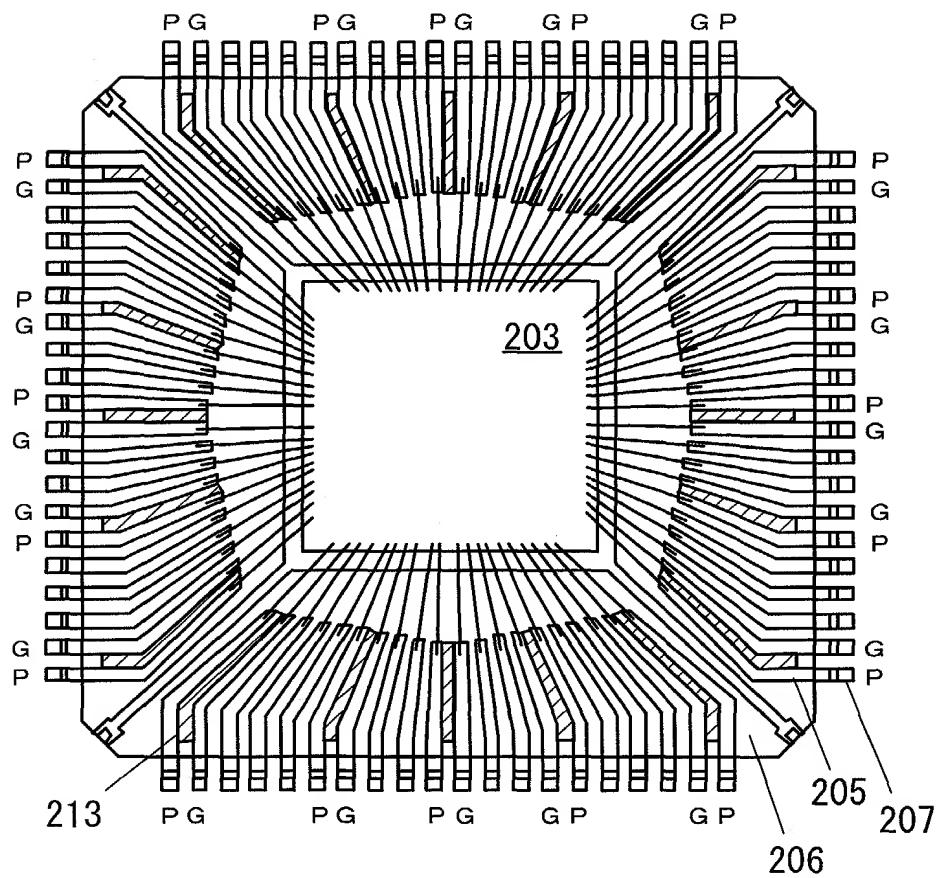
FIG. 5

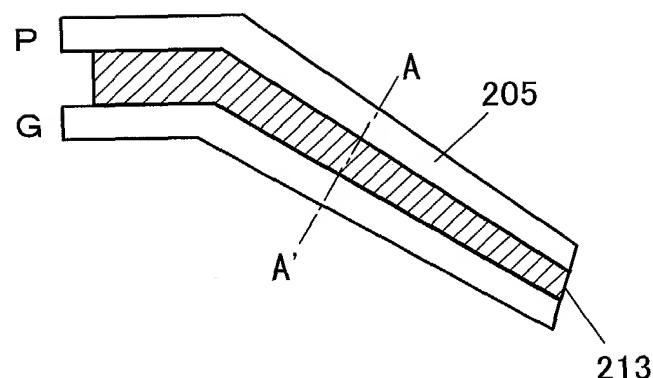
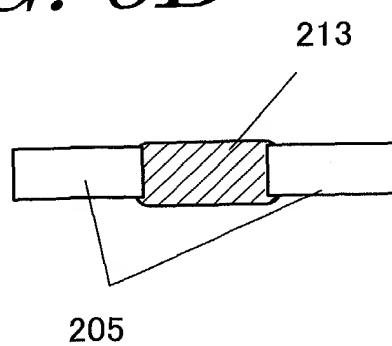
FIG. 6A*FIG. 6B*

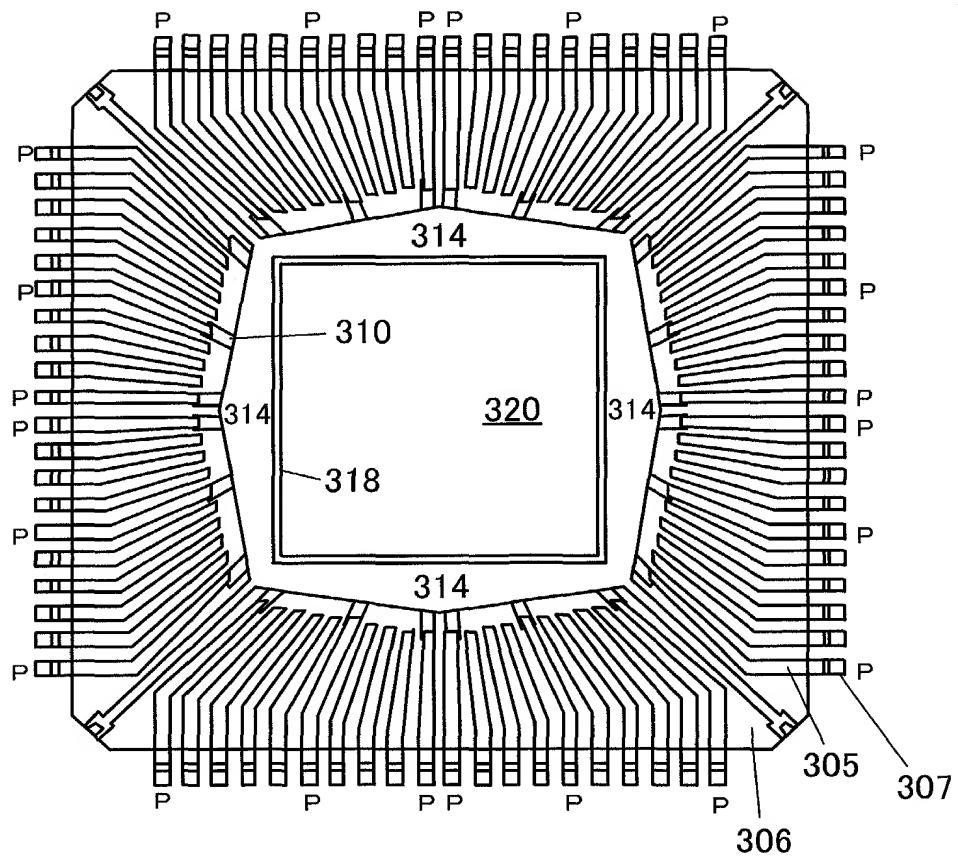
FIG. 7

FIG. 8A

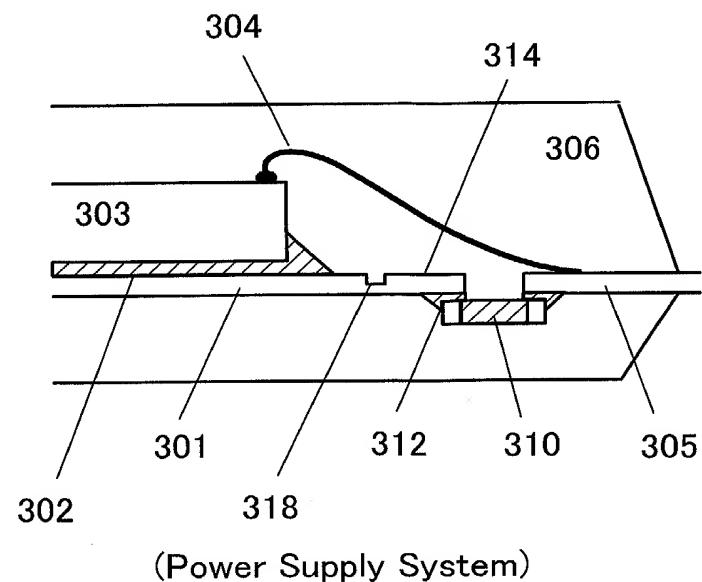


FIG. 8B

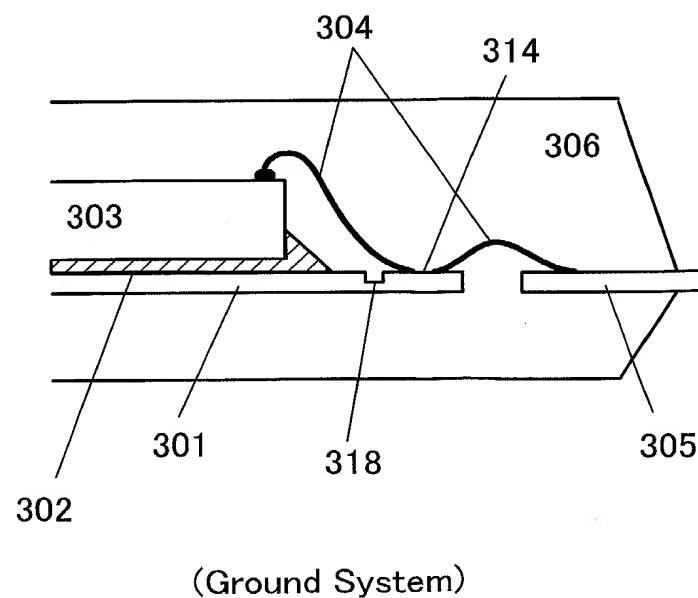


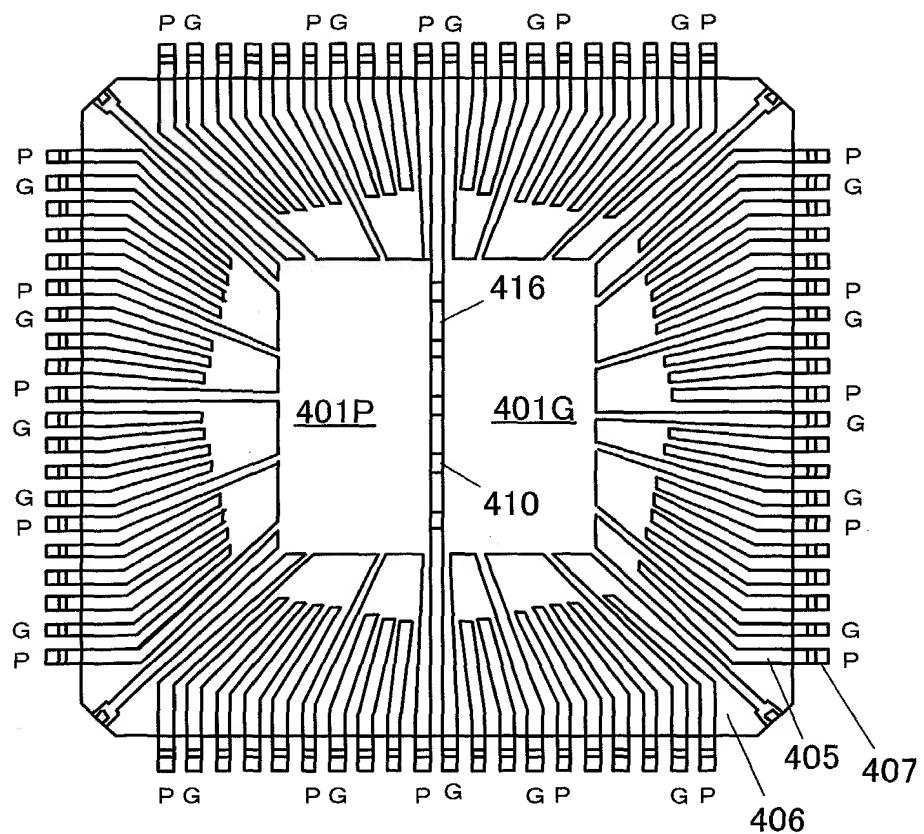
FIG. 9

FIG. 10

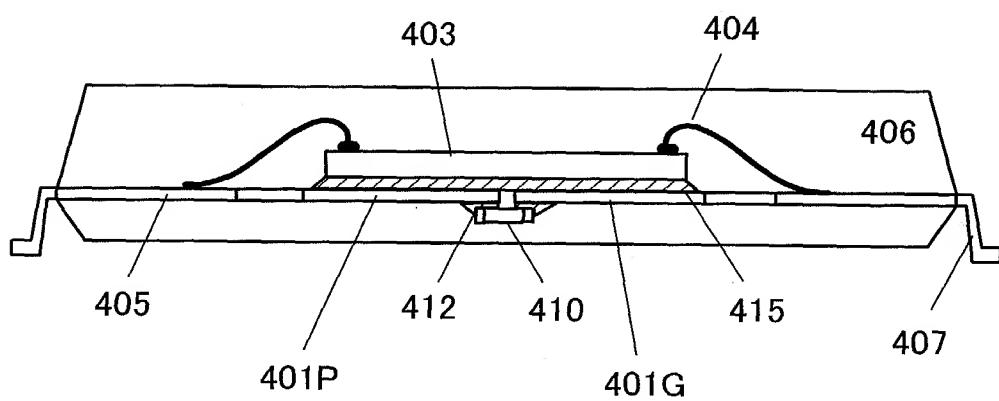


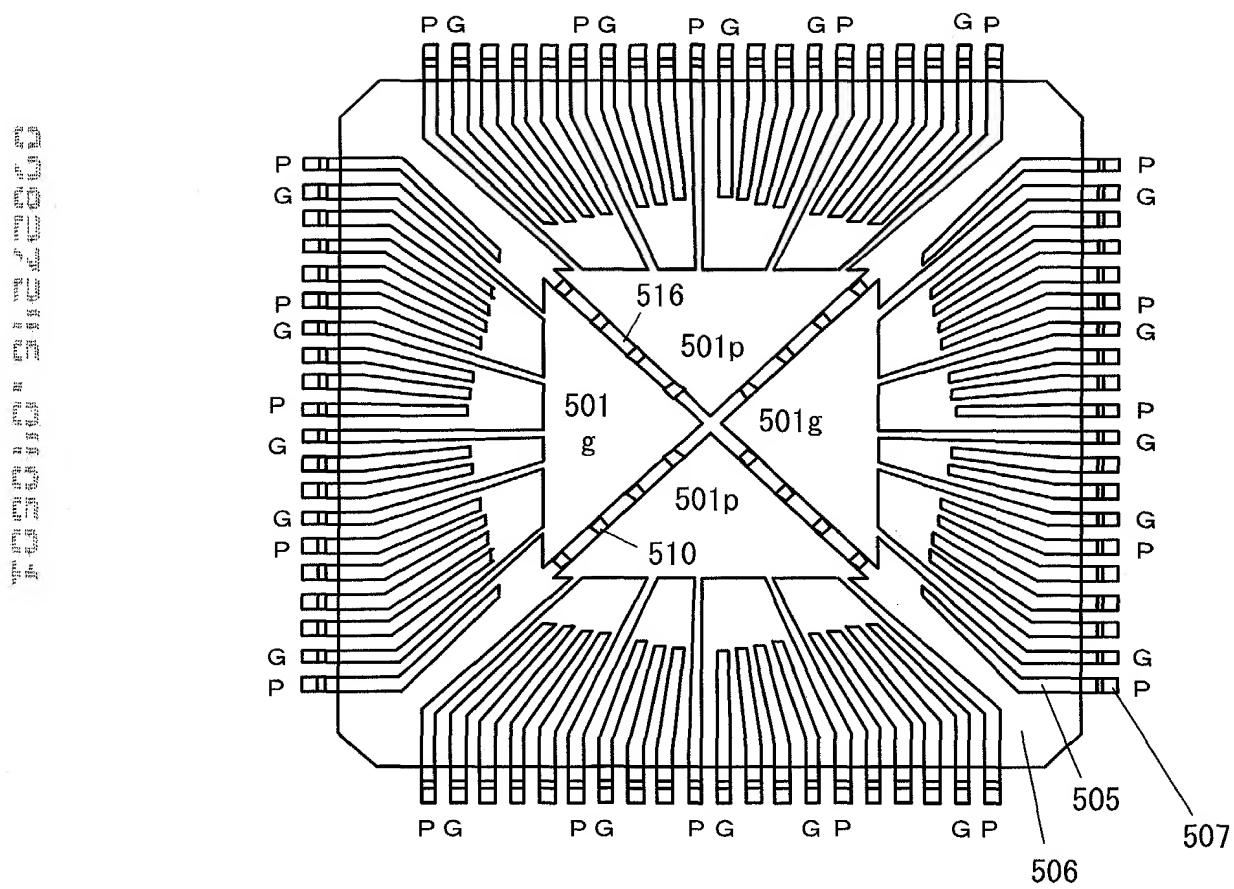
FIG. 11

FIG. 12

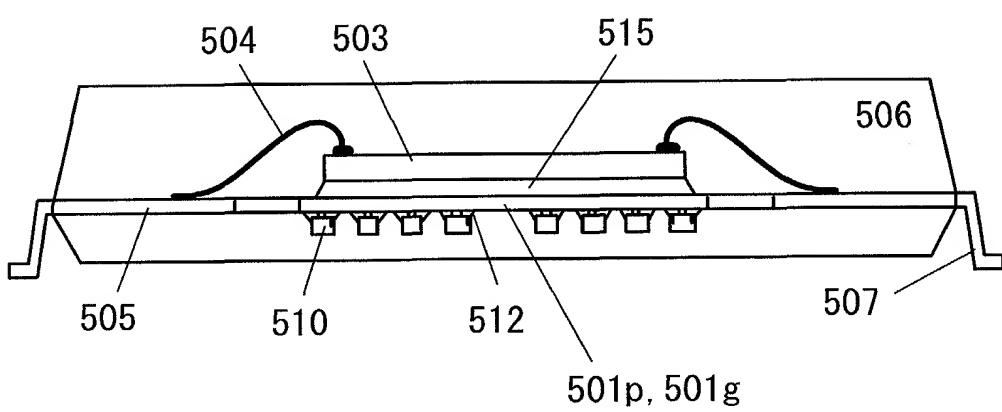


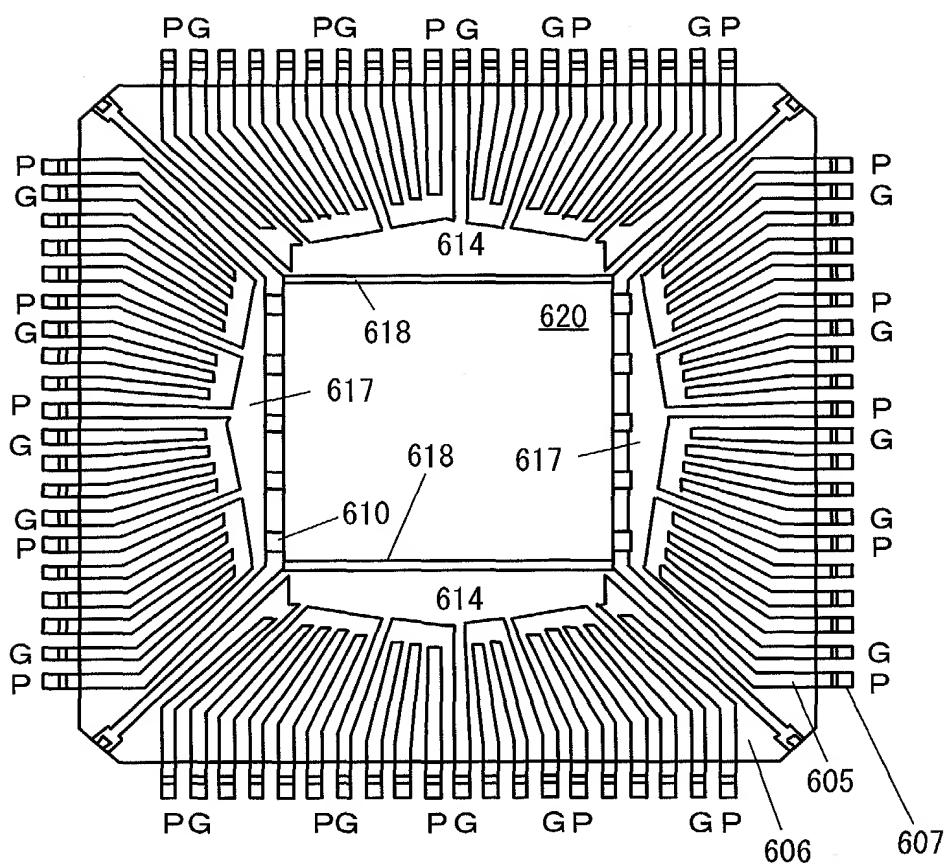
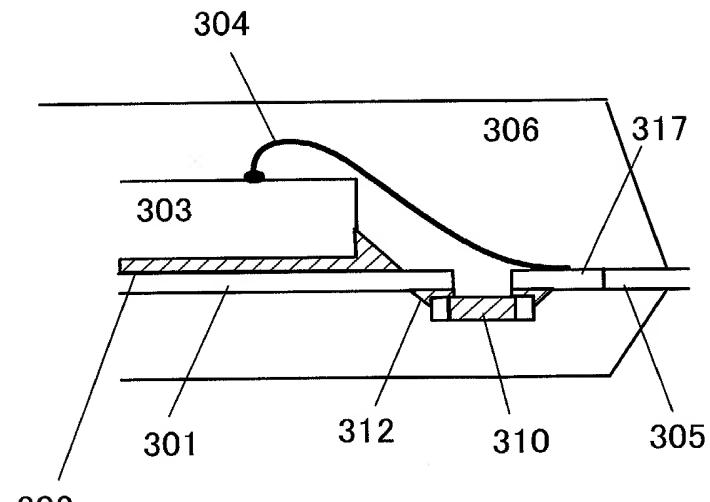
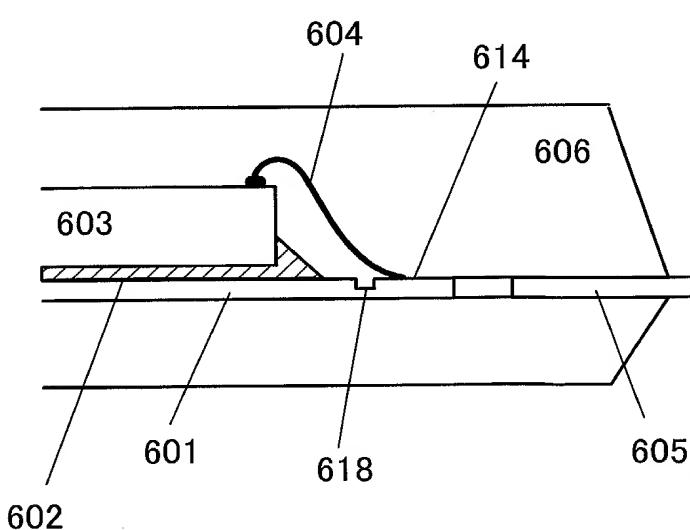
FIG. 13

FIG. 14A



(Power Supply System)

FIG. 14B



(Ground System)

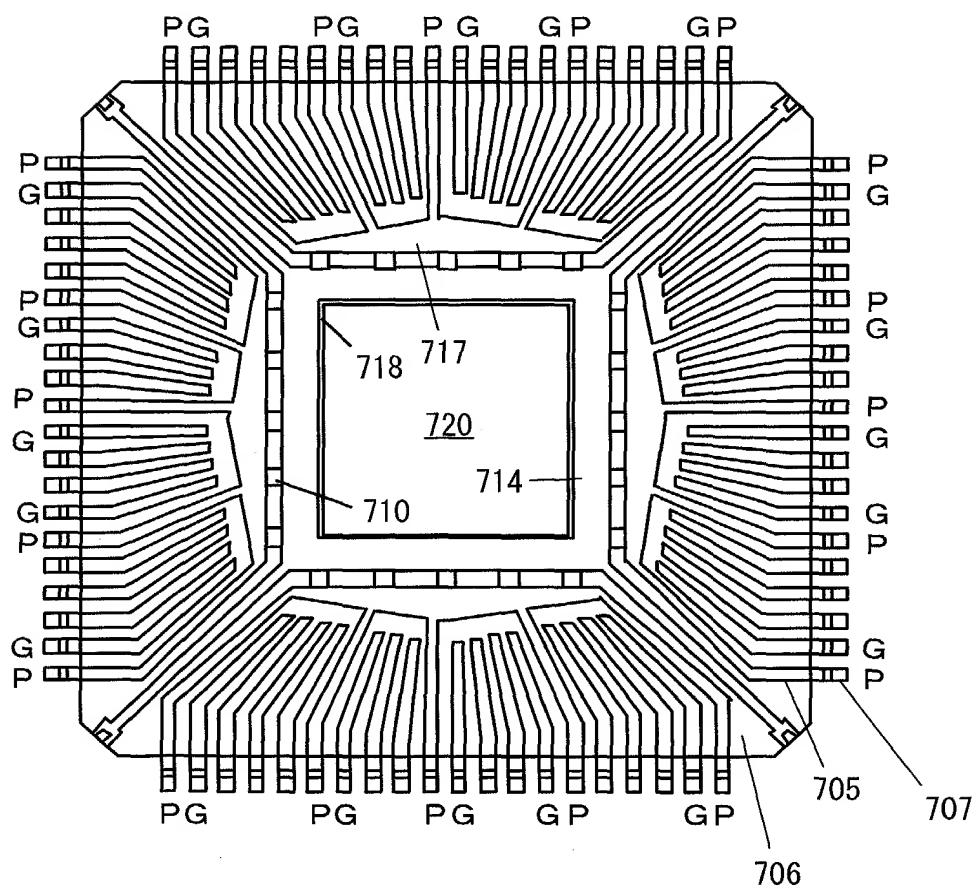
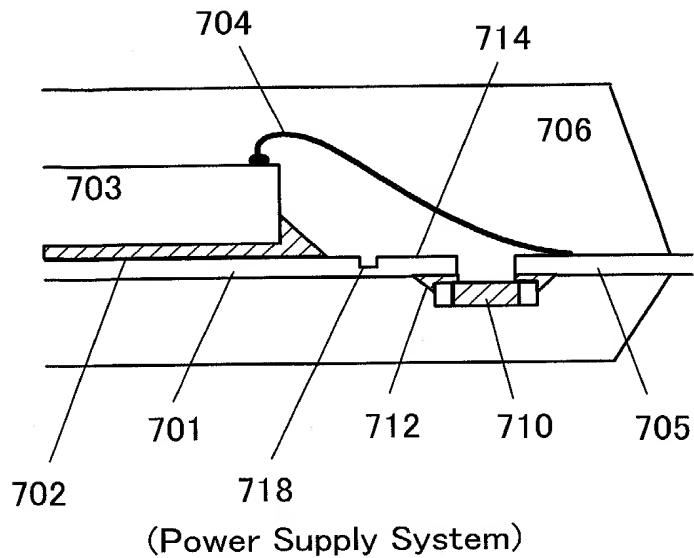
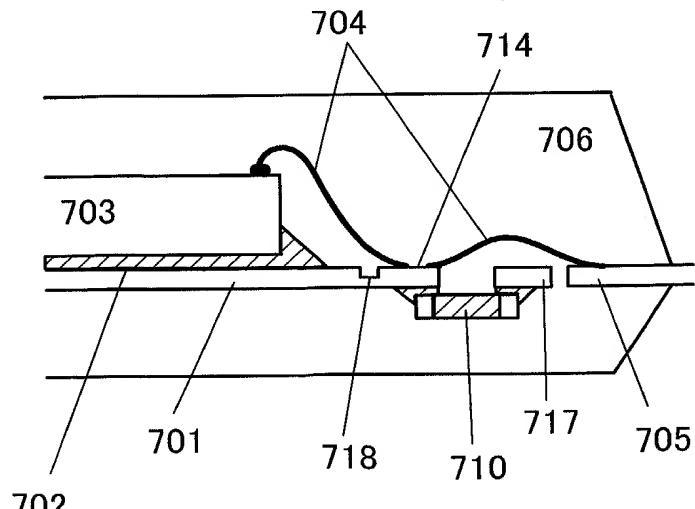
FIG. 15

FIG. 16A



(Power Supply System)

FIG. 16B



(Ground System)

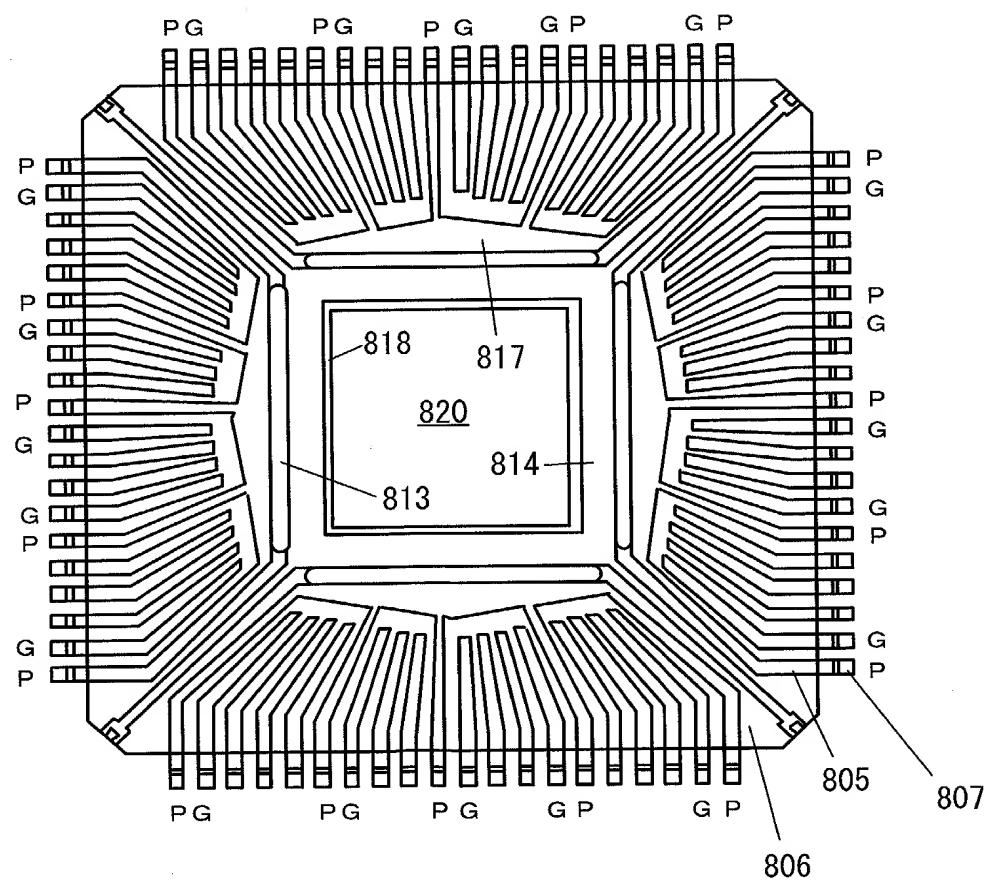
FIG. 17

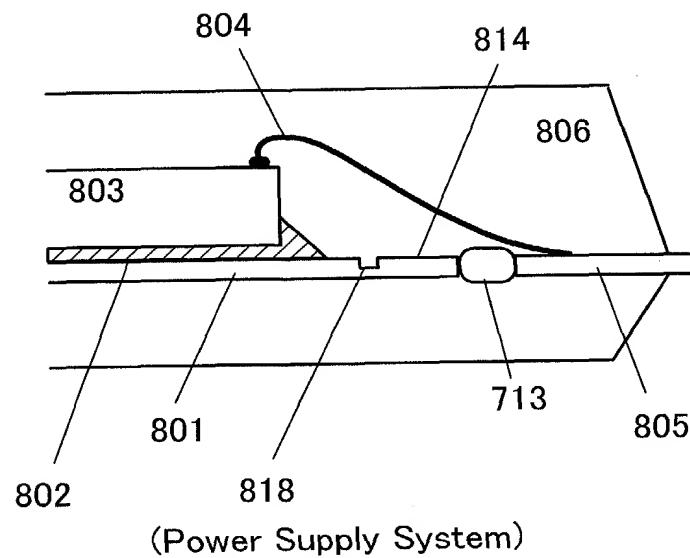
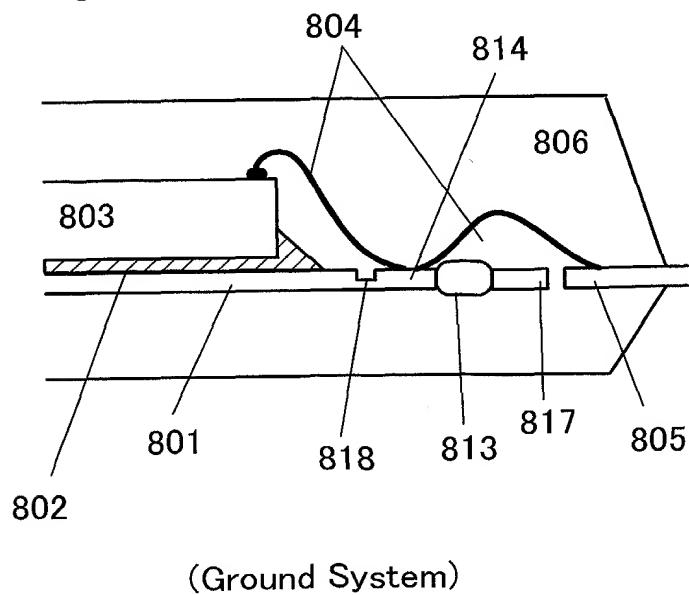
FIG. 18A*FIG. 18B*

FIG. 19

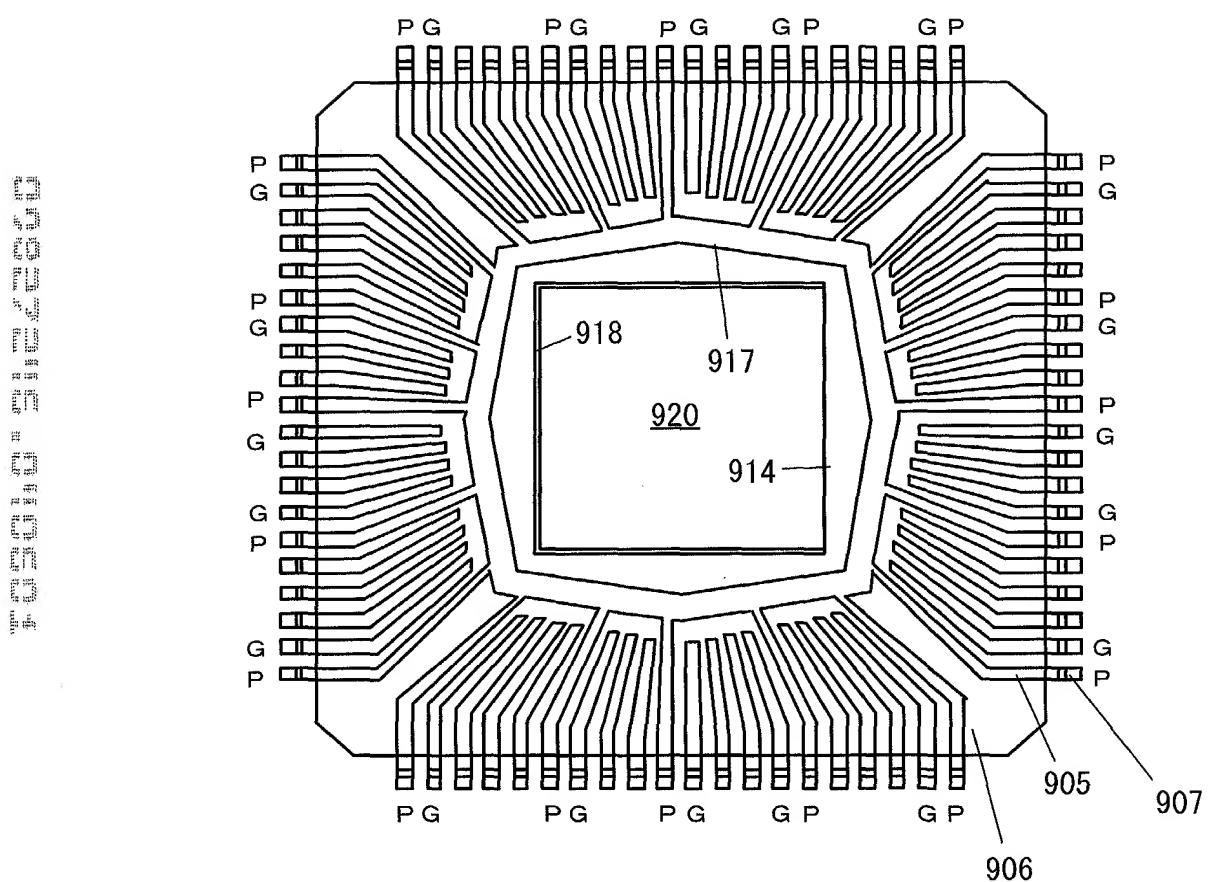


FIG. 20A

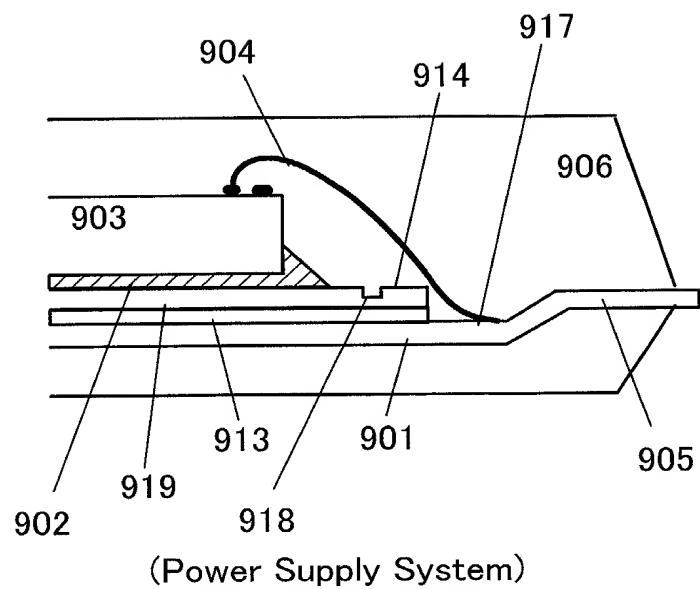
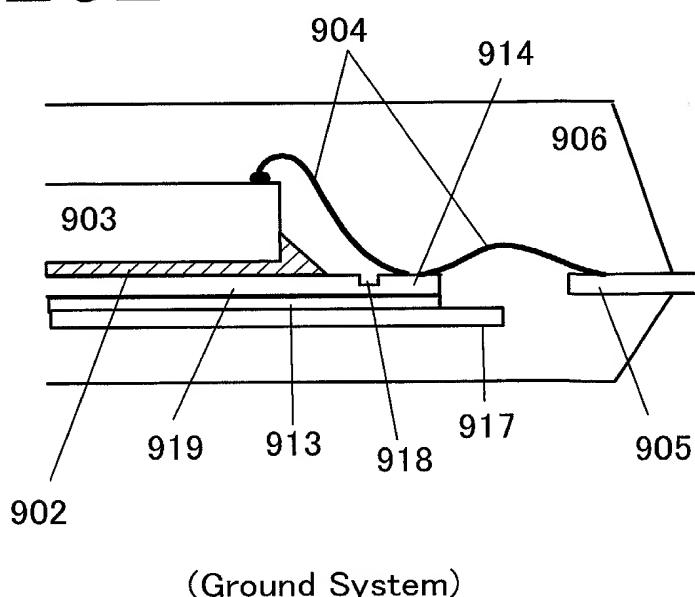


FIG. 20B



(Ground System)

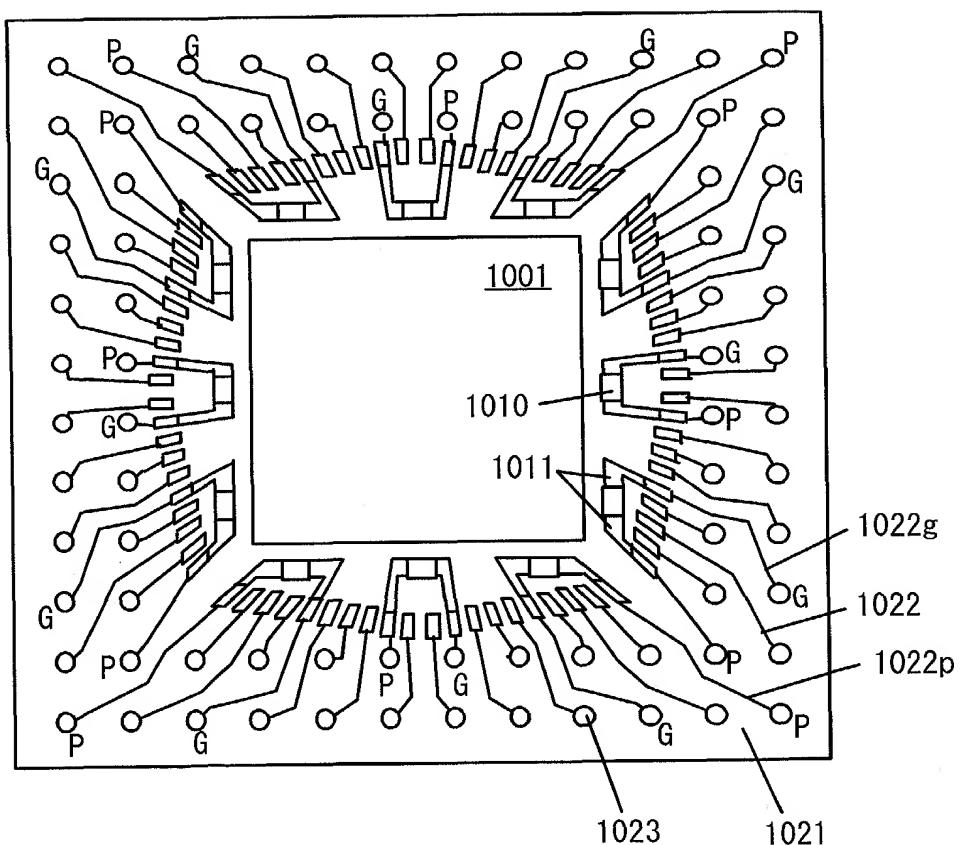
FIG. 21

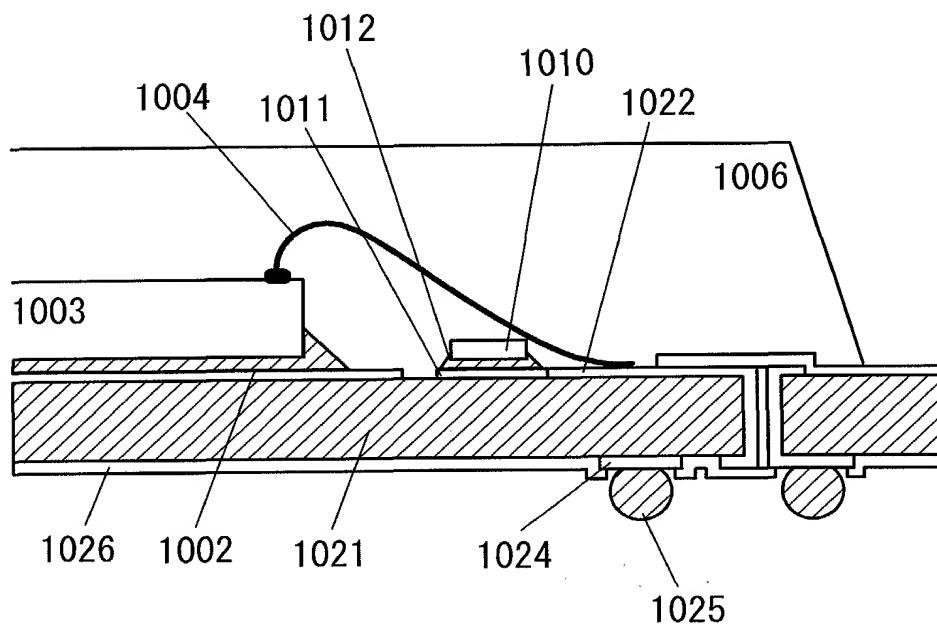
FIG. 22

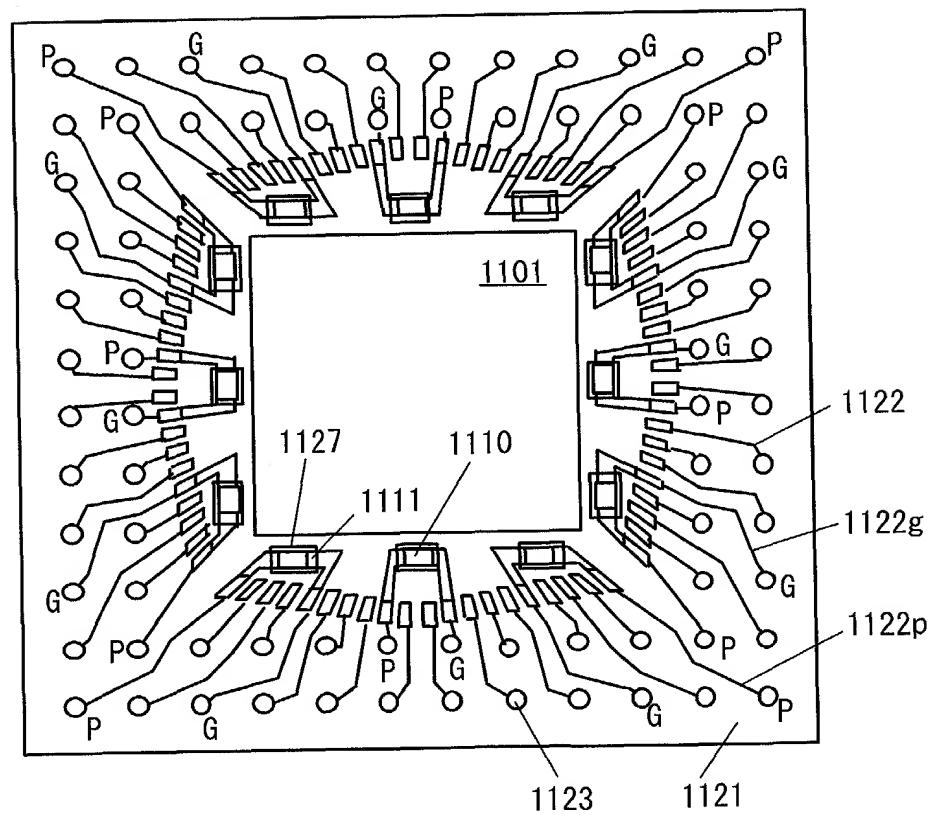
FIG. 23

FIG. 24

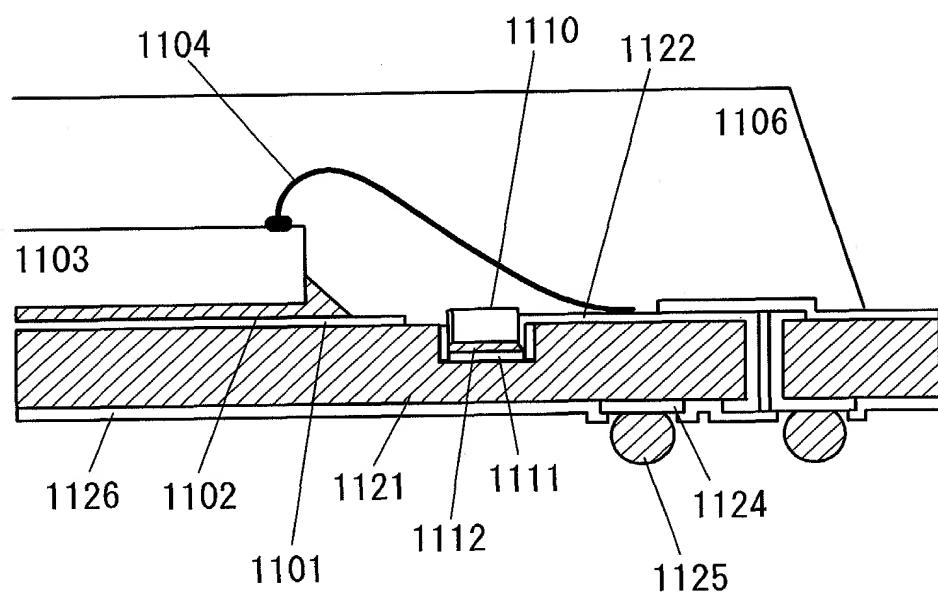


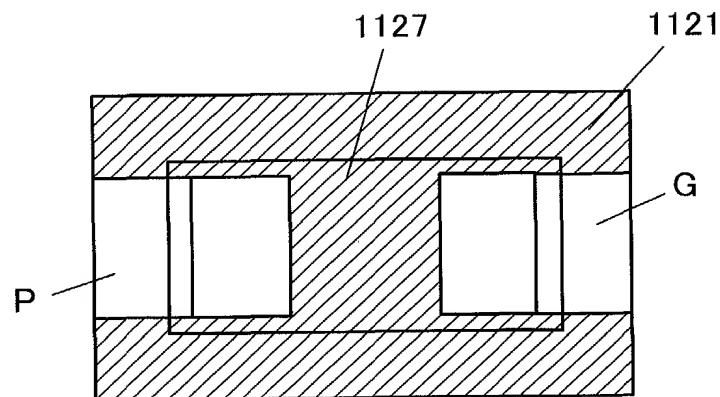
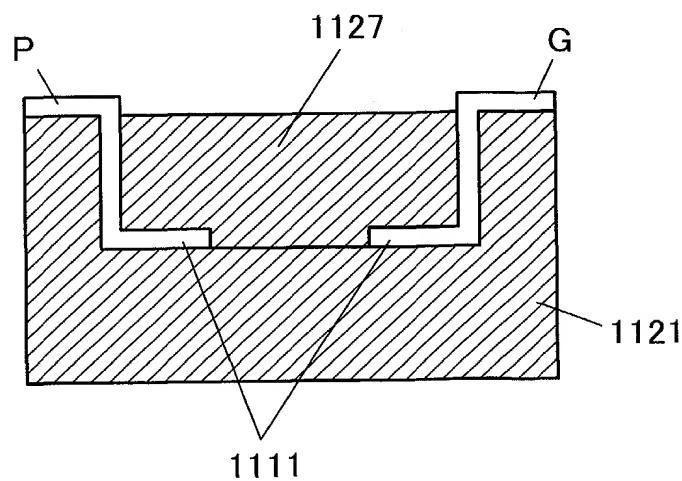
FIG. 25A*FIG. 25B*

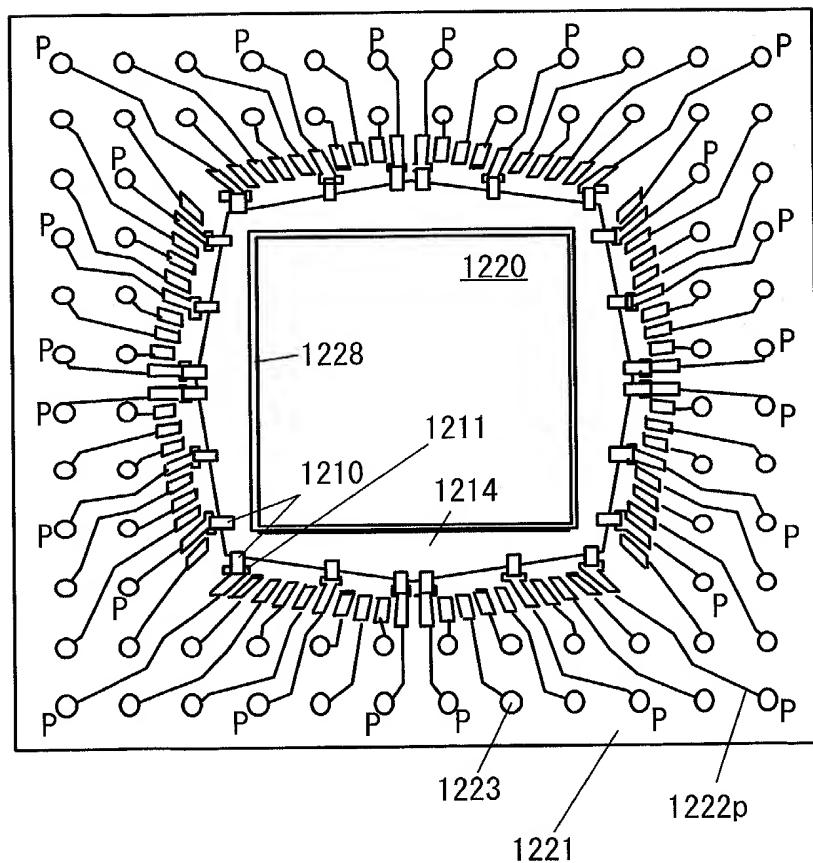
FIG. 26

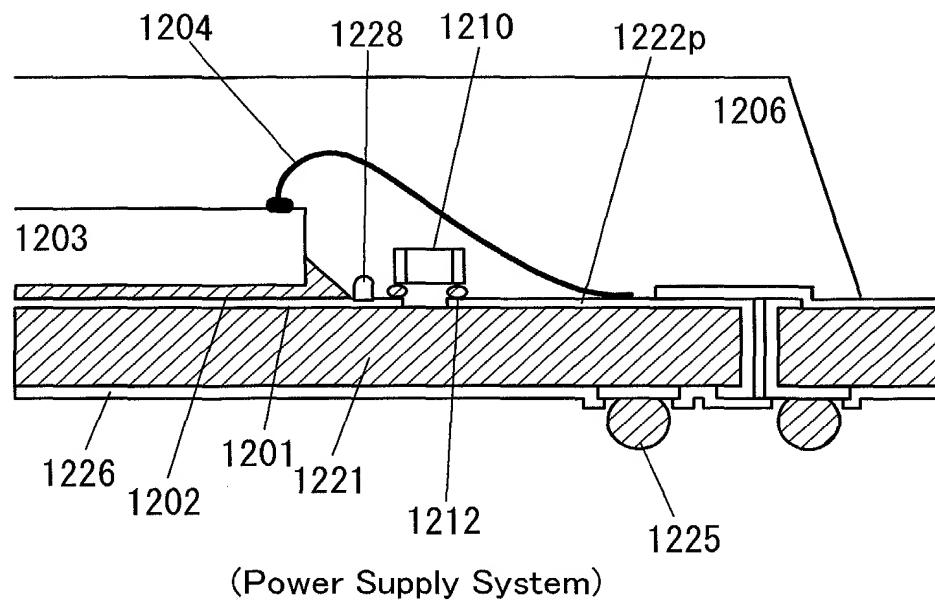
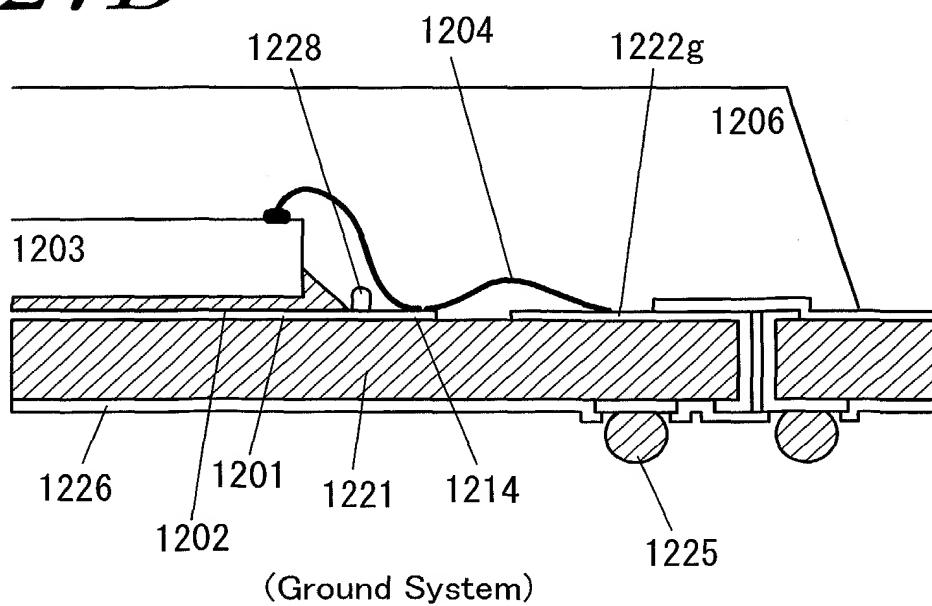
FIG. 27A*FIG. 27B*

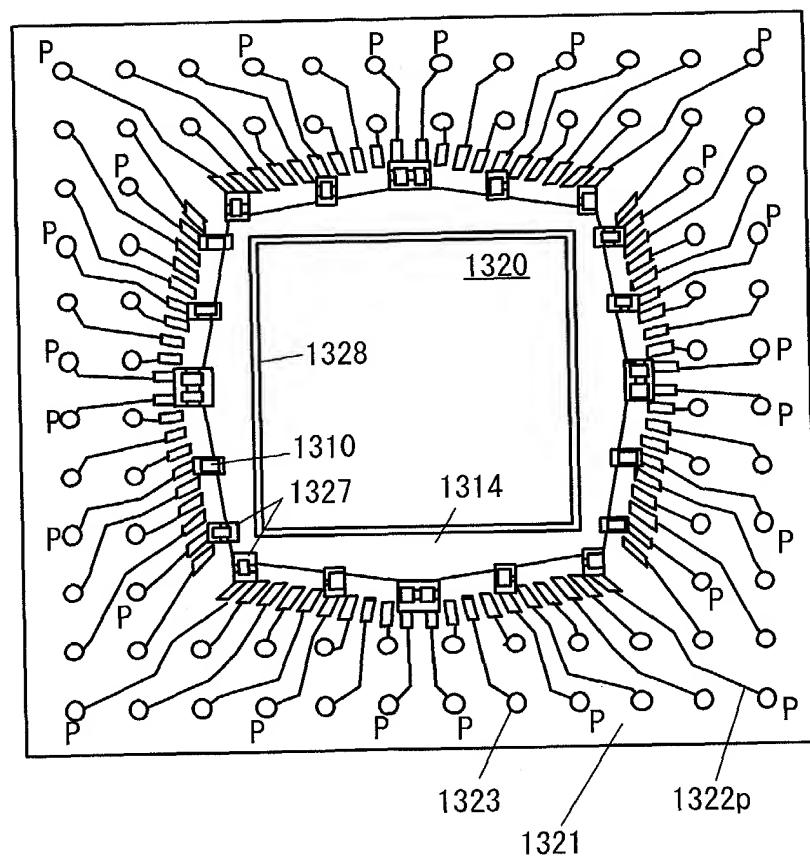
FIG. 28

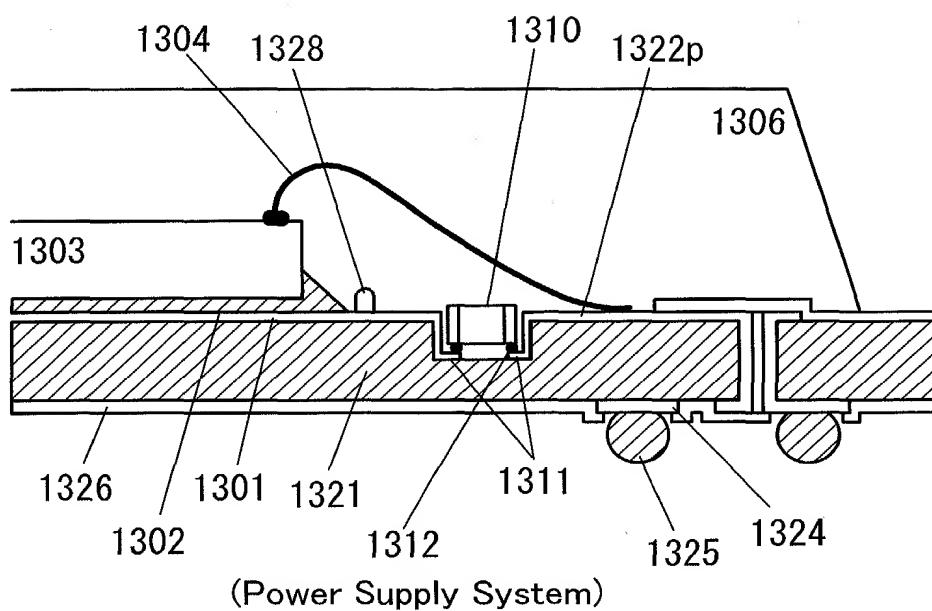
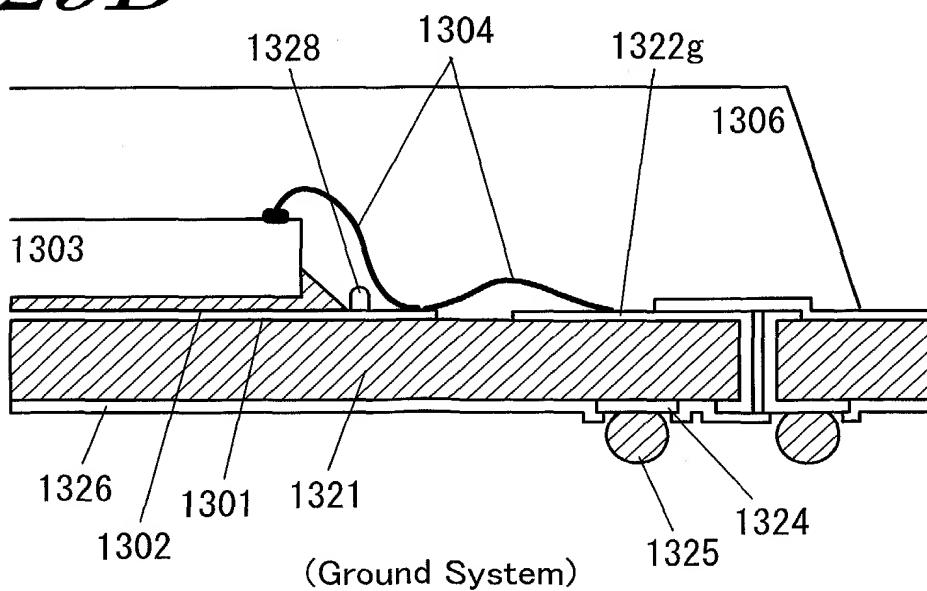
FIG. 29A*FIG. 29B*

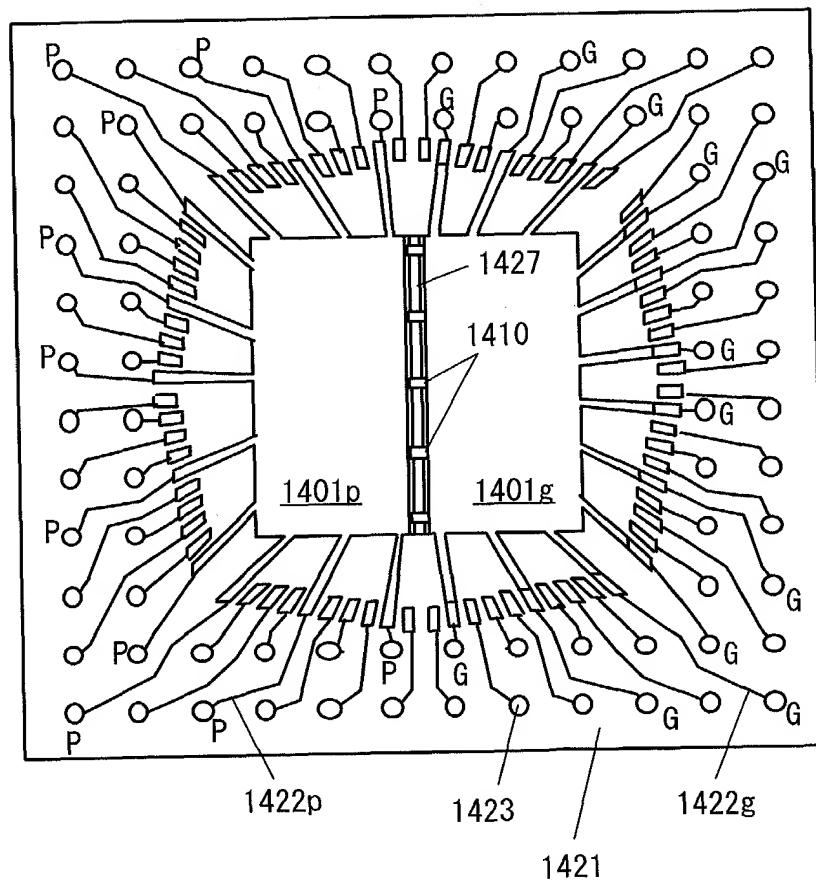
FIG. 30

FIG. 31

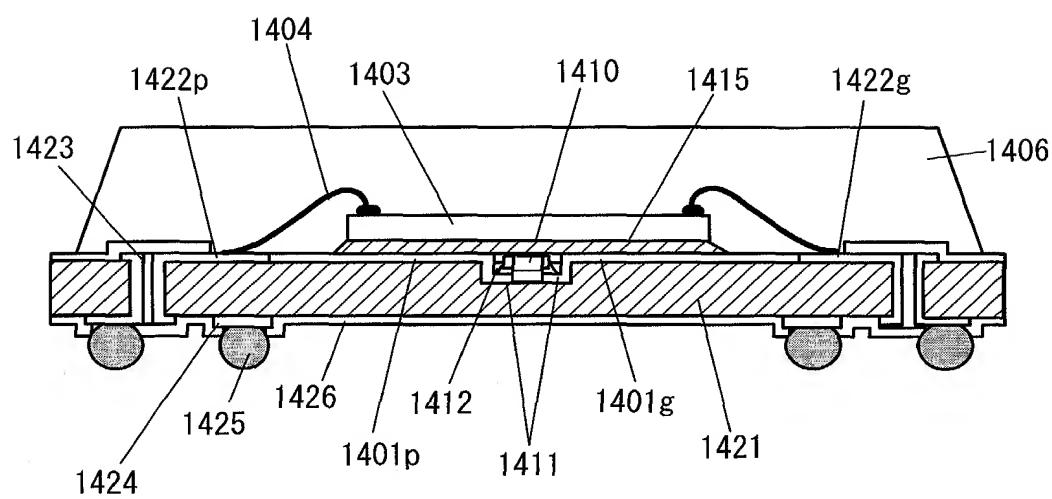


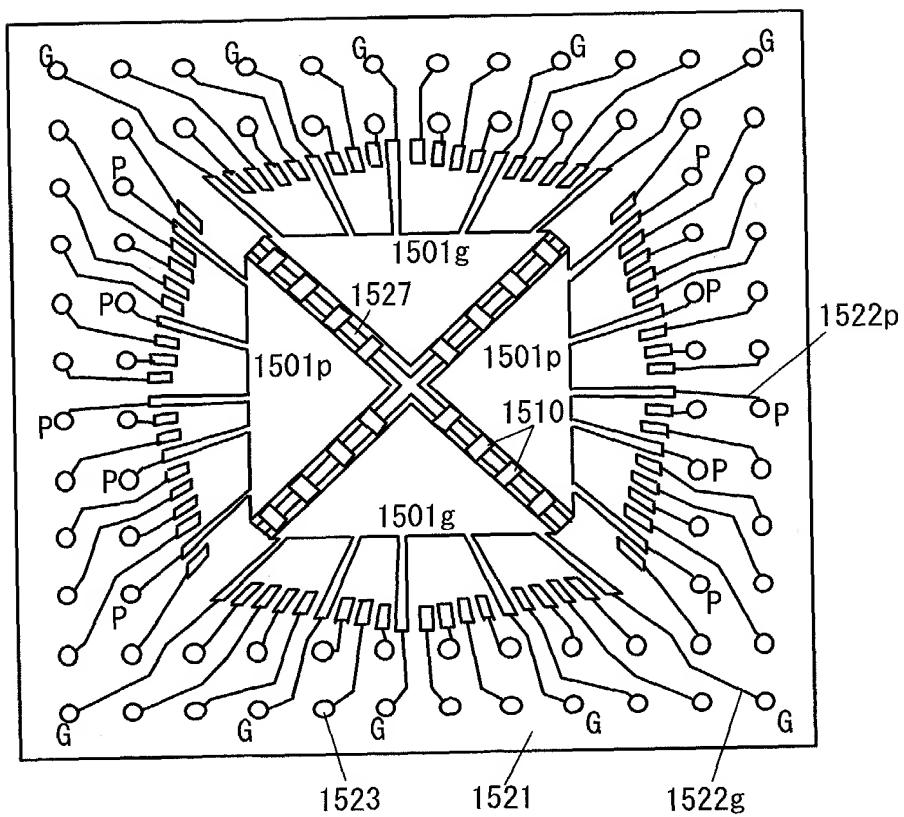
FIG. 32

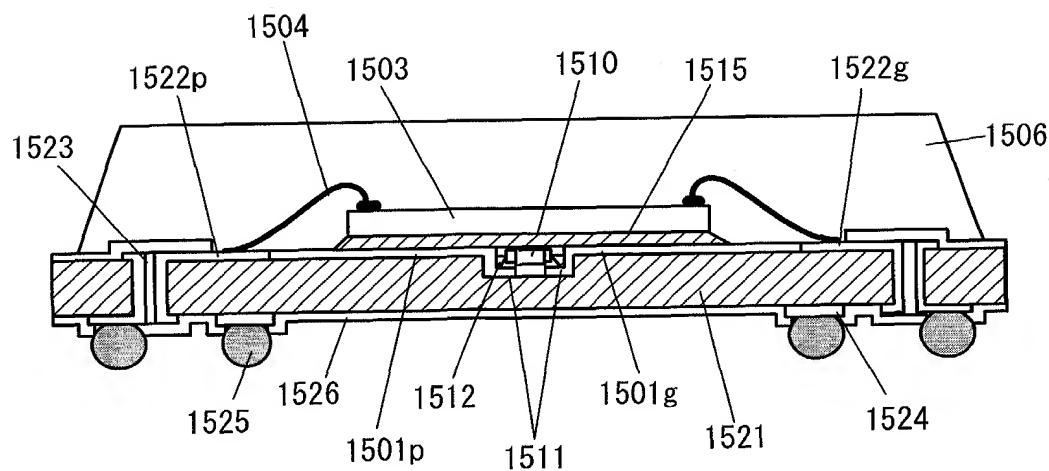
FIG. 33

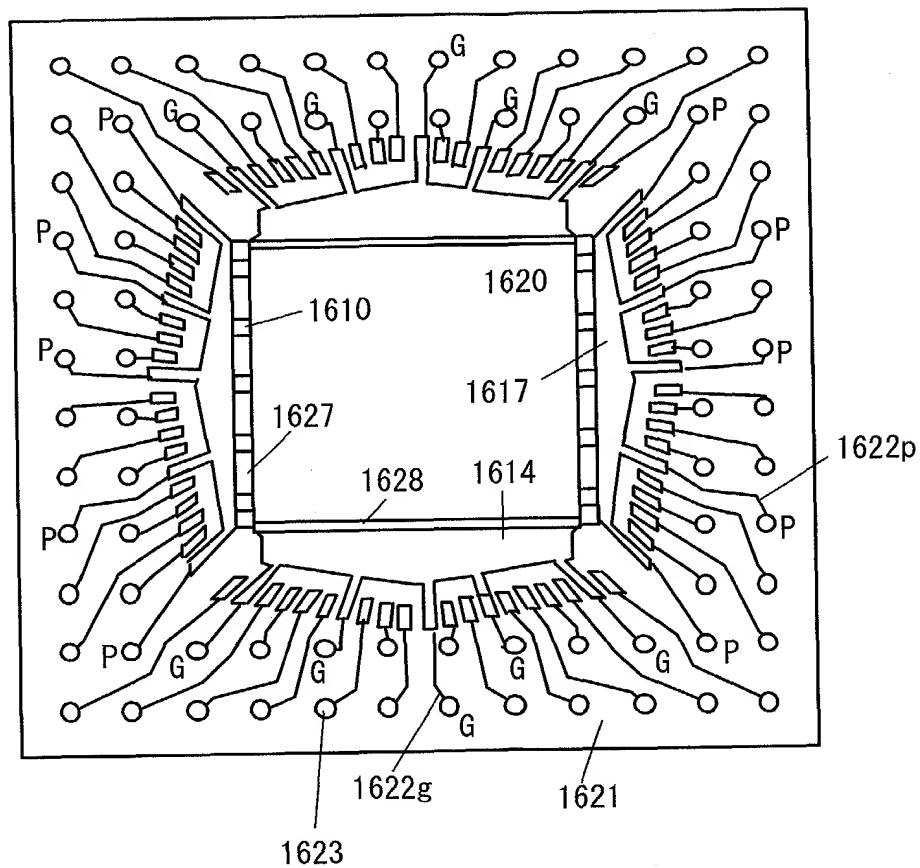
FIG. 34

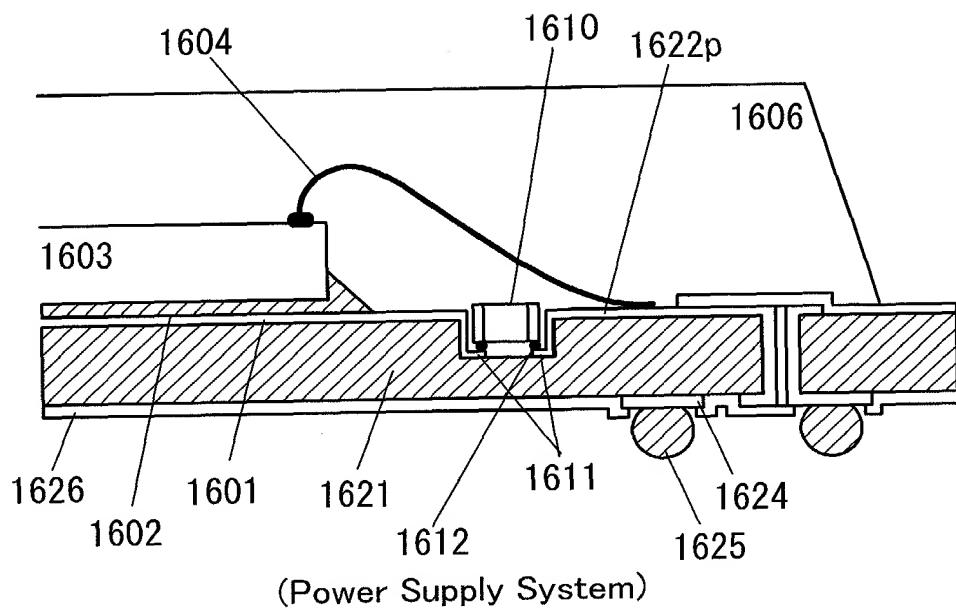
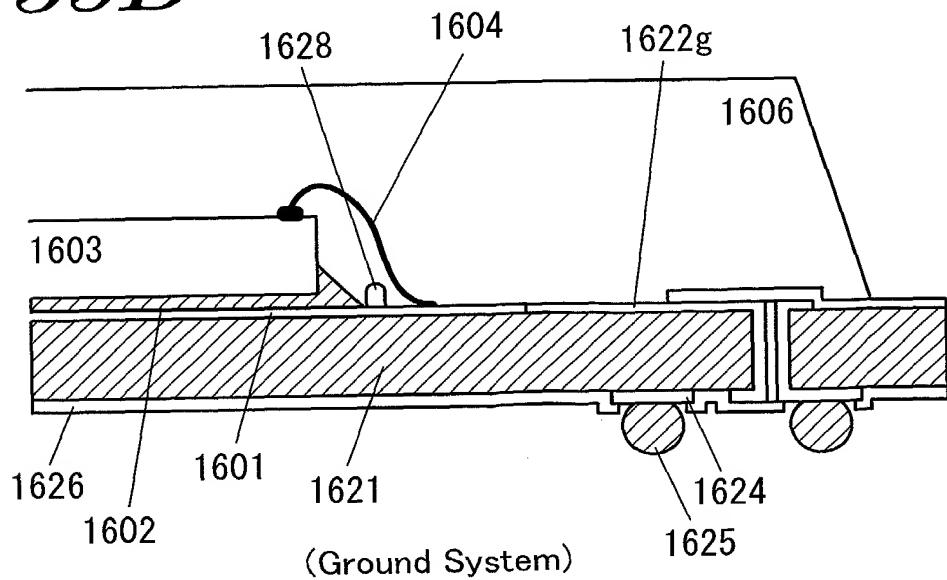
FIG. 35A*FIG. 35B*

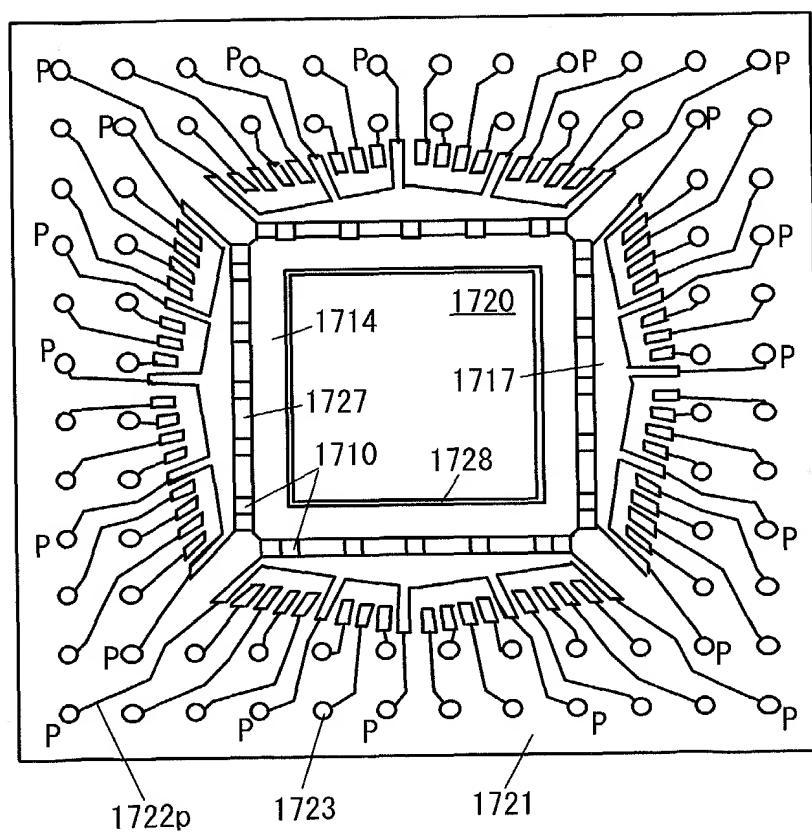
FIG. 36

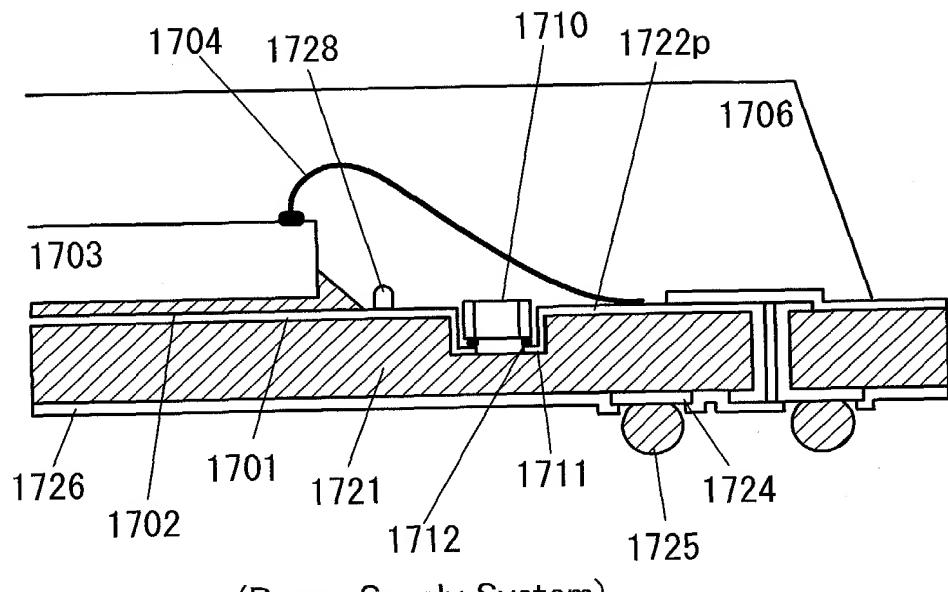
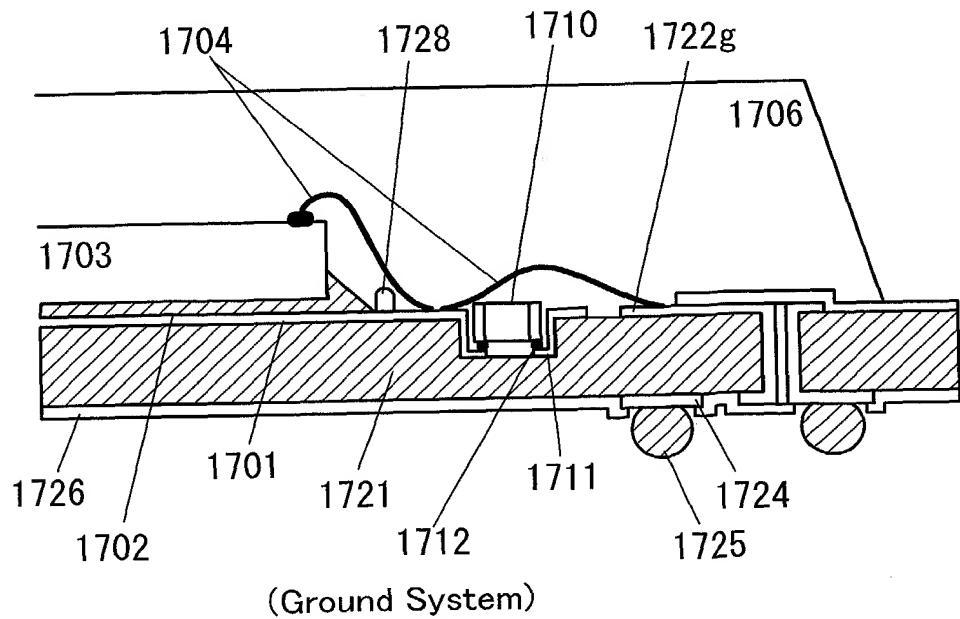
FIG. 37A*FIG. 37B*

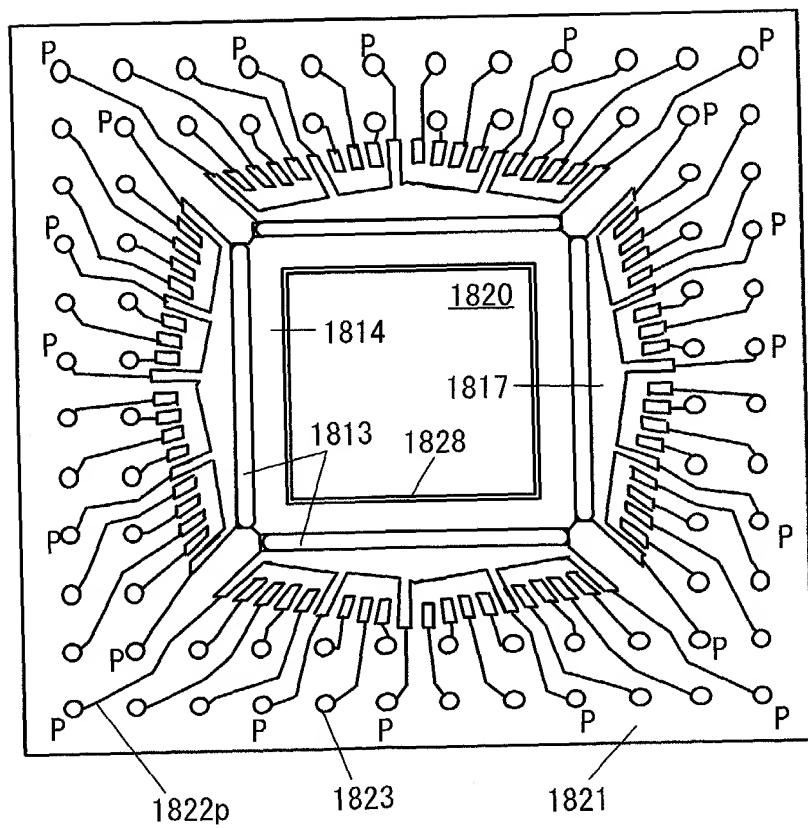
FIG. 38

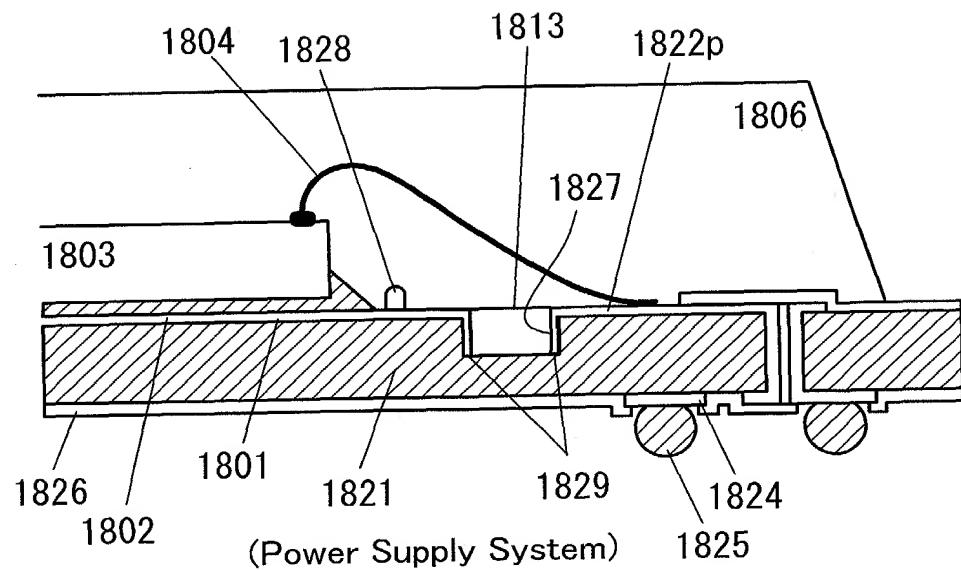
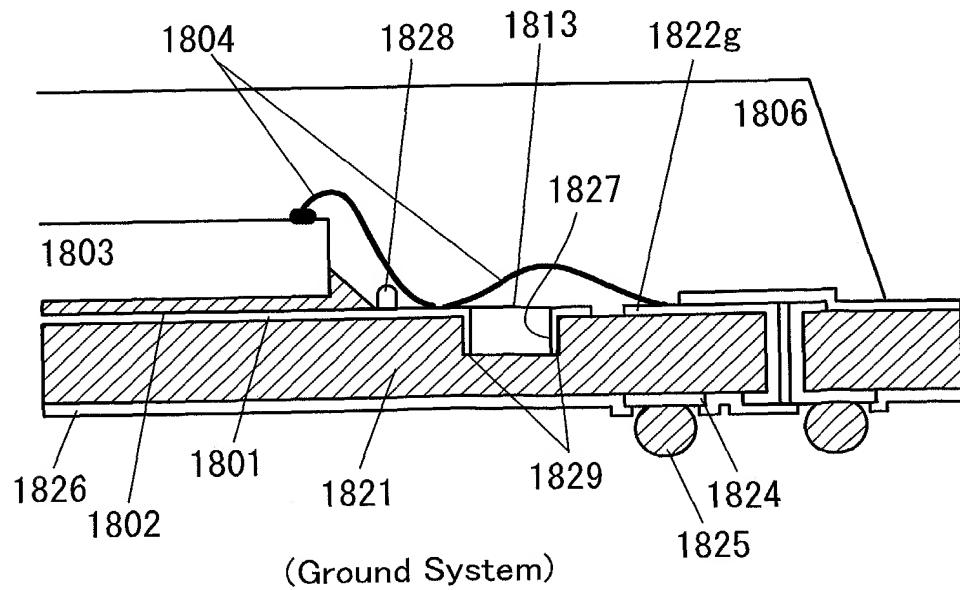
FIG. 39A*FIG. 39B*

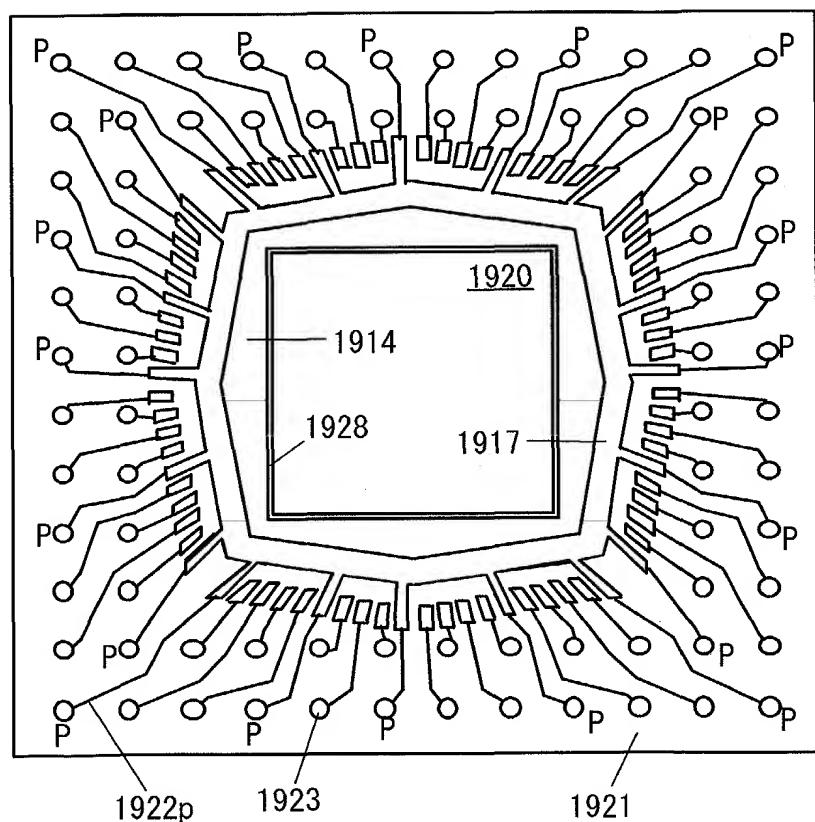
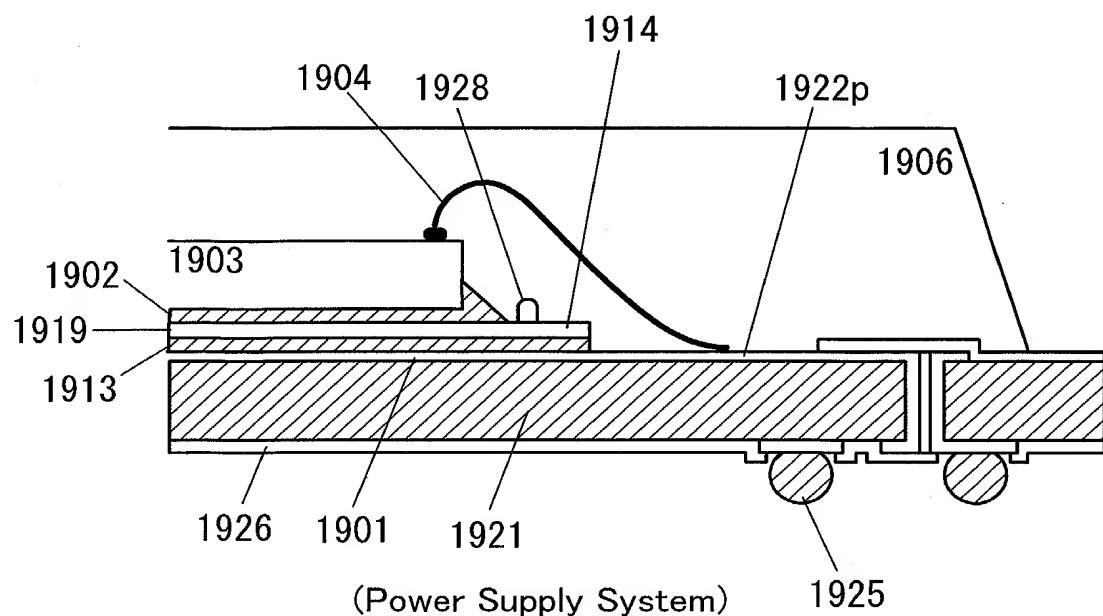
FIG. 40

FIG. 41A*FIG. 41B*